DATAFORTH[®]

DSCT36 Potentiometer Input Transmitters

Description

Each DSCT36 potentiometer input transmitter provides a single channel of potentiometer input which is filtered, isolated, amplified, and converted to a process current output (Figure 1). Signal filtering is accomplished with a five-pole filter, which provides 85dB of normal-mode rejection at 60Hz and 80dB at 50Hz. An anti-aliasing pole is located on the field side of the isolation barrier, and the other four are on the process loop side. After the initial field-side filtering, the input signal is chopped by a proprietary chopper circuit. Isolation is provided by transformer coupling, again using a proprietary technique to suppress transmission of common mode spikes or surges.

Potentiometer excitation is provided from the transmitter using a precision current source. The excitation current is small (less than 0.26mA) which minimizes self-heating of the potentiometer. Lead compensation is achieved by matching two current paths which cancels the effects of lead resistance.

Special input and output circuits on the DSCT36 transmitters provide protection against accidental connection of power-line voltages up to 240VAC and against transient events as defined by ANSI/IEEE C37.90.1. Signal and loop power lines are secured to the module using screw terminals, which are in pluggable terminal blocks for ease of system assembly and reconfiguration.

The modules have excellent stability over time and do not require recalibration, however, zero and span settings are adjustable up to $\pm 10\%$ to accommodate situations where fine-tuning is desired. The adjustments are made using potentiometers located under the front panel label and are non-interactive for ease of use.

Features

- Interfaces to Potentiometers up to $10 k \Omega$
- Process Current Output
- 1500Vrms Transformer Isolation
- ANSI/IEEE C37.90.1 Transient Protection
- Input and Output Protected to 240VAC Continuous
- Up to 60V Loop Voltage
- 160dB CMR
- 85dB NMR at 60Hz, 80dB at 50Hz
- ±0.03% Accuracy
- ±0.01% Linearity
- Easily Mounts on Standard DIN Rail
- CSA C/US Certified
- CE Compliant



Figure 1: DSCT36 Block Diagram

Specifications Typical* at T_A = +25°C and +24VDC loop voltage

	•	A	
	Module	DSCT36	Model
	Input Range Input Resistance Normal Power Off Overload Input Protection Continuous Transient CMV, Input to Output Continuous Transient CMR (50Hz or 60Hz) NMR	0Ω to 10kΩ 50MΩ 66kΩ 240Vrms max ANSI/IEEE C37.90.1 1500Vrms max ANSI/IEEE C37.90.1 160dB 85dB at 60Hz, 80dB at 50Hz	DSCT36- DSCT36- DSCT36- DSCT36-
	Adjustability Accuracy ⁽¹⁾ Conformity Stability Offset Gain Sensor Excitation Current	±10% Zero and Span ±0.03% ±0.01% ±50ppm/°C ±100ppm/°C 0.26mA; 100Ω, 500Ω Sensor 0.13mA; 1kΩ Sensor 0.065mA; 10kΩ Sensor	
	Lead Resistance Effect	$\pm 0.01\Omega/\Omega$; 100Ω, 500Ω, 1kΩ Sensor $\pm 0.02\Omega/\Omega$; 10kΩ Sensor	
	Noise Output, 100kHz Bandwidth, –3dB Response Time, 90% Span	3µArms 3Hz 165ms	
	Output Range Output Limits Under-range Over-range Output Protection Reverse Polarity Over-voltage Transient Loop Supply Voltage Loop Supply Sensitivity Turn-On Delay	4mA to 20mA 3mA 29mA Continuous 240Vrms Continuous ANSI/IEEE C37.90.1 10.8V to 60V ±0.0005%/V 400ms	
	Mechanical Dimensions (h)(w)(d)	2.95" x 0.89" x 4.13" (75mm x 22.5mm x 105mm)	
	Mounting	DIN EN 50022 -35x7.5 or -35x15 rail	
	Environmental Operating Temperature Storage Temperature Relative Humidity Emissions, EN61000-6-4 Radiated, Conducted Immunity EN61000-6-2 RF ESD, EFT	-40°C to +80°C -40°C to +80°C 0 to 95% Noncondensing ISM, Group 1 Class A ISM, Group 1 Performance A ±0.5% Span Error Performance B	

NOTES:

*Contact factory or your local Dataforth sales office for maximum values.

(1) Includes linearity, hysteresis and repeatability.

Model Input Range

Ordering Information

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DSCT36-01 DSCT36-02 DSCT36-03	0 to 100Ω 0 to 500Ω 0 to 1kΩ
DSCT36-04	0 to 10kΩ